

LISTING OF THE CLAIMS

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

1. (Original) A hybrid digital broadcasting receiver for reproducing digital multimedia data, comprising:
 - a broadcast receiving module comprising:
 - a receiving section for receiving and demodulating a digital broadcasting data stream which includes a multiplexed and transmitted plurality of compressively encoded and scrambled programs;
 - a first demultiplexer for demultiplexing said demodulated digital broadcasting data stream, and selecting and extracting digital broadcasting data corresponding to a program selected by a user;
 - a conditional access section for detecting conditional access information and decrypting said selected digital broadcasting data using said detected information; and
 - a decoder module comprising:
 - a second demultiplexer for demultiplexing a digital multimedia data stream which includes a multiplexed plurality of compressively encoded digital multimedia data; and
 - a decoding section for decoding digital broadcasting data output from said broadcast receiving module and digital multimedia data output from said second demultiplexer.
2. (Original) The hybrid digital broadcasting receiver according to claim 1, further comprising a smart card for receiving said conditional access information and generating a scrambling key.

3. (Currently Amended) The hybrid digital broadcasting receiver according to claim 1, wherein said conditional access information comprises Entitlement Control Message (ECM)~~program management information~~ and Entitlement Management Message (EMM)~~subscriber management information~~.

4. (Original) The hybrid digital broadcasting receiver according to claim 2, wherein said conditional access section receives said scrambling key from said smart card and decrypts said digital broadcasting data.

5. (Original) The hybrid digital broadcasting receiver according to claim 1, further comprising a multimedia module for supplying said digital multimedia data stream to said second demultiplexer.

6. (Original) The hybrid digital broadcasting receiver according to claim 1, wherein said digital multimedia data comprises audio data and video data.

7. (Original) The hybrid digital broadcasting receiver according to claim 6, wherein said second demultiplexer separates said audio data and said video data from said digital multimedia data stream.

8. (Original) The hybrid digital broadcasting receiver according to claim 1, wherein said broadcast receiving module and said decoder module are each formed in a single integrated circuit.

9. (Original) A hybrid digital broadcasting receiver for reproducing digital multimedia data, comprising:

a receiving section for receiving a digital broadcasting data stream which includes multiplexed and transmitted digital broadcasting data packets and conditional access information packets for a plurality of programs;

a first demultiplexer for separating said conditional access information packets and digital broadcasting data packets for a program selected by a user from said received digital broadcasting data stream;

a conditional access section for detecting conditional access information from said conditional access information packets and decrypting said separated digital broadcasting data packets using said conditional access information;

a second demultiplexer for receiving a digital multimedia data stream which includes multiplexed compressively encoded audio packets and video packets, and separating said audio packets and said video packets from said digital multimedia data stream; and

a decoding section for decoding digital broadcasting data packets output from said conditional access section and audio packets and video packets output from said second demultiplexer.

10. (Original) The hybrid digital broadcasting receiver according to claim 9, further comprising a smart card for receiving said conditional access information and generating a scrambling key.

11. (Currently Amended) The hybrid digital broadcasting receiver according to claim 9, wherein said conditional access information comprises Entitlement Control Message (ECM)~~program management information~~ and Entitlement Management Message (EMM)~~subscriber management information~~.

12. (Original) The hybrid digital broadcasting receiver according to claim 10, wherein said conditional access section receives said scrambling key from said smart card and decrypts said digital broadcasting data.

13. (Original) The hybrid digital broadcasting receiver according to claim 9, further comprising a multimedia module for supplying said digital multimedia data stream to said second demultiplexer.

14. (Original) The hybrid digital broadcasting receiver according to claim 9, wherein said receiving section, first demultiplexer and conditional access section are formed in a first integrated circuit chip while said second demultiplexer and decoding section are formed in a second integrated circuit chip.

15. (Currently Amended) ~~A device~~ An integrated chip having at least one surface for processing digital broadcasting data, the integrated chip comprising:

a receiving section disposed on said at least one surface of said integrated chip for receiving and demodulating a digital broadcasting data stream which includes multiplexed and transferred digital broadcasting data packets and conditional access information packets for a plurality of programs;

an error correcting section disposed on said at least one surface of said integrated chip for correcting any error in said demodulated digital broadcasting data stream;

a demultiplexer disposed on said at least one surface of said integrated chip for separating said conditional access information packets and digital broadcasting data packets for a program selected by a user from said demodulated digital broadcasting data stream; and

a conditional access section disposed on said at least one surface of said integrated chip and in electrical communication with said demultiplexer for detecting conditional access information from said conditional access information packets and decrypting said separated digital broadcasting data packets using said conditional access information.

16. (Currently Amended) ~~A device~~ An integrated chip having at least one surface for processing digital broadcasting data, the integrated chip comprising:

a receiving section disposed on said at least one surface of said integrated chip for receiving and demodulating a digital broadcasting data stream which includes

multiplexed and transferred digital broadcasting data packets and conditional access information packets for a plurality of programs;

an error correcting section disposed on said at least one surface of said integrated chip for correcting any error in said demodulated digital broadcasting data stream;

a demultiplexer disposed on said at least one surface of said integrated chip for separating said conditional access information packets and digital broadcasting data packets for a program selected by a user from said demodulated digital broadcasting data stream; and

a conditional access section disposed on said at least one surface of said integrated chip and in electrical communication with said demultiplexer for detecting conditional access information from said conditional access information packets and decrypting said separated digital broadcasting data packets using said conditional access information.

17. (Currently Amended) An integrated chip according to claim 15, further comprising a smart card interface for outputting said conditional access information to a smart card and receiving a scrambling key generated by said smart card.

18. (Currently Amended) An integrated chip ~~The device~~ according to claim 17, wherein said conditional access section receives said scrambling key from said smart card and decrypts said digital broadcasting data.

19. (Currently Amended) An integrated chip ~~The device~~ according to claim 15, further comprising a decoder module interface for supplying said decrypted digital broadcasting data to said decoder module.

20. (Currently Amended) An integrated chip ~~The device~~ according to claim 15, wherein said device is formed in a single integrated circuit chip.

21-24. (Canceled)